

FIG.1

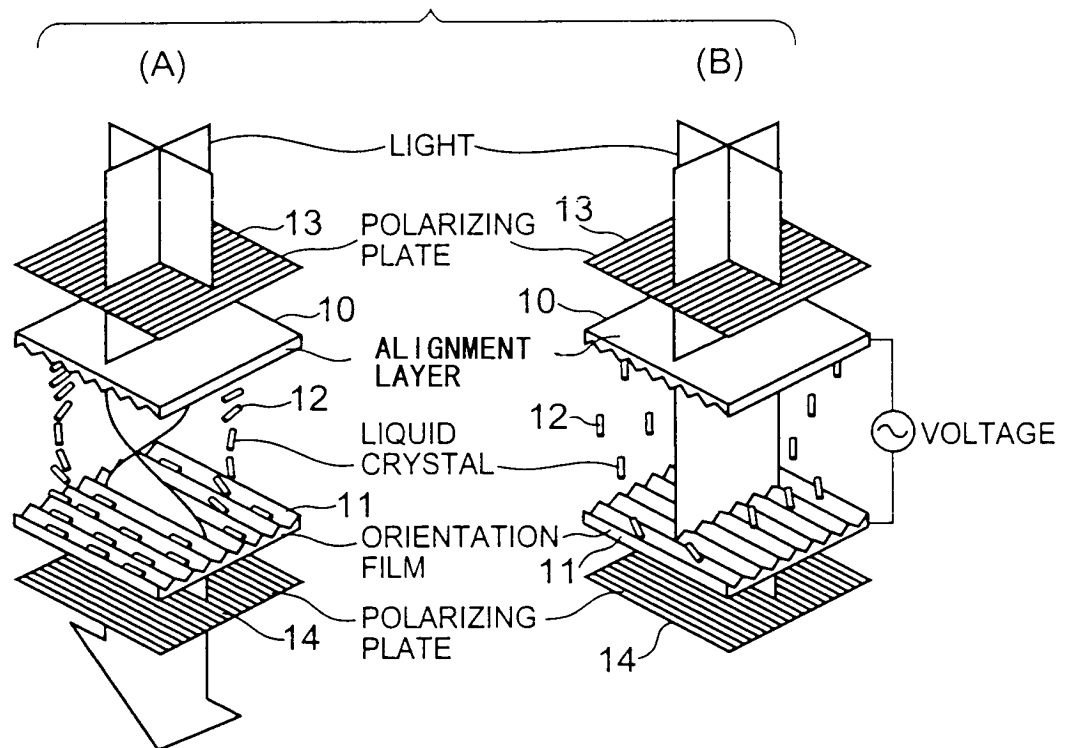


FIG.2

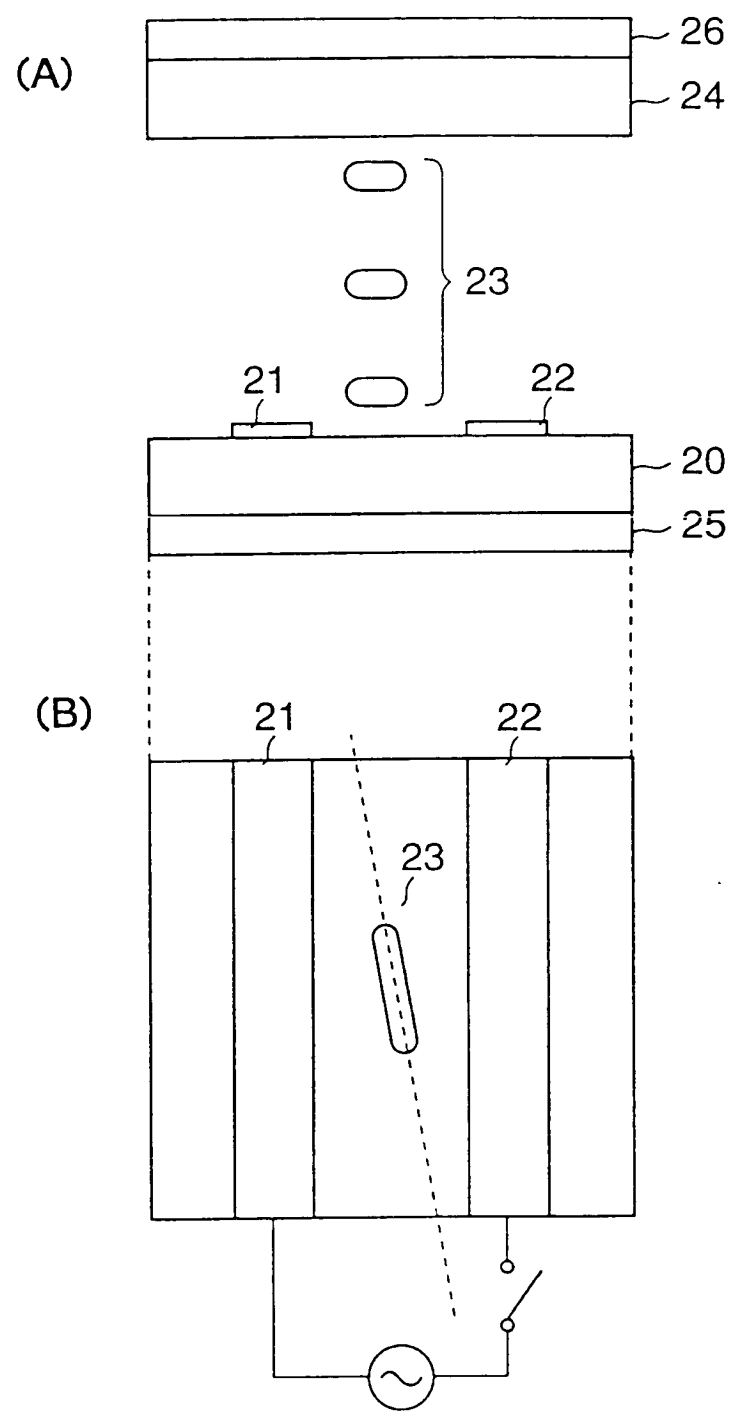


FIG.3

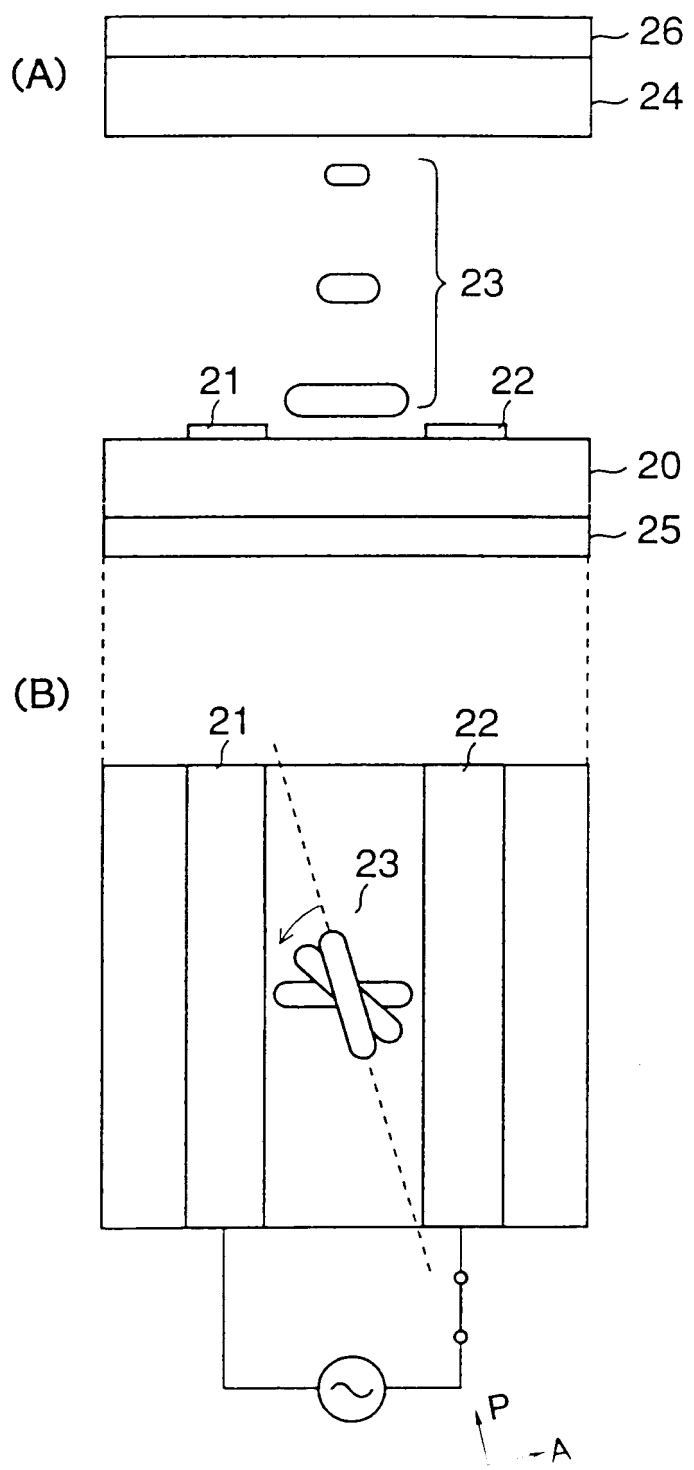


FIG.4

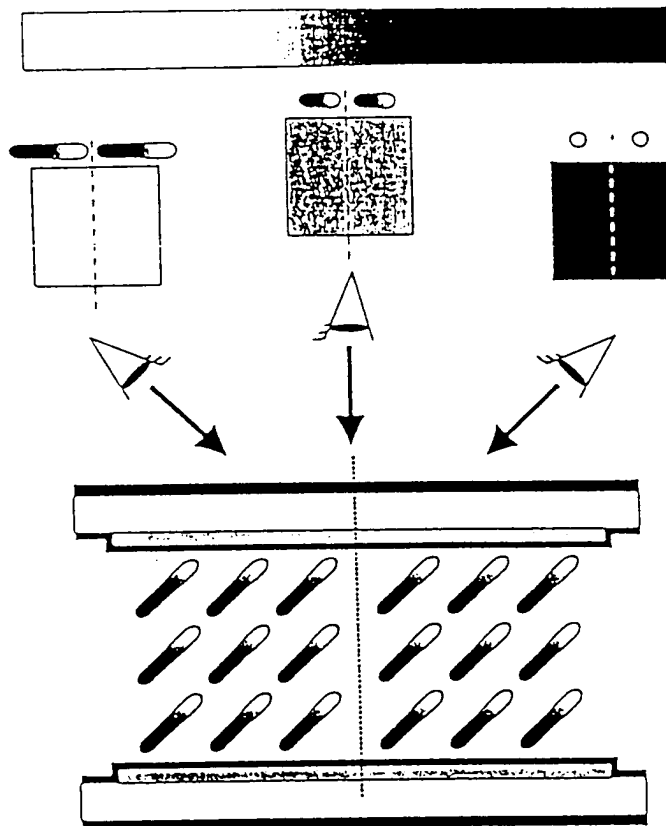


FIG.5

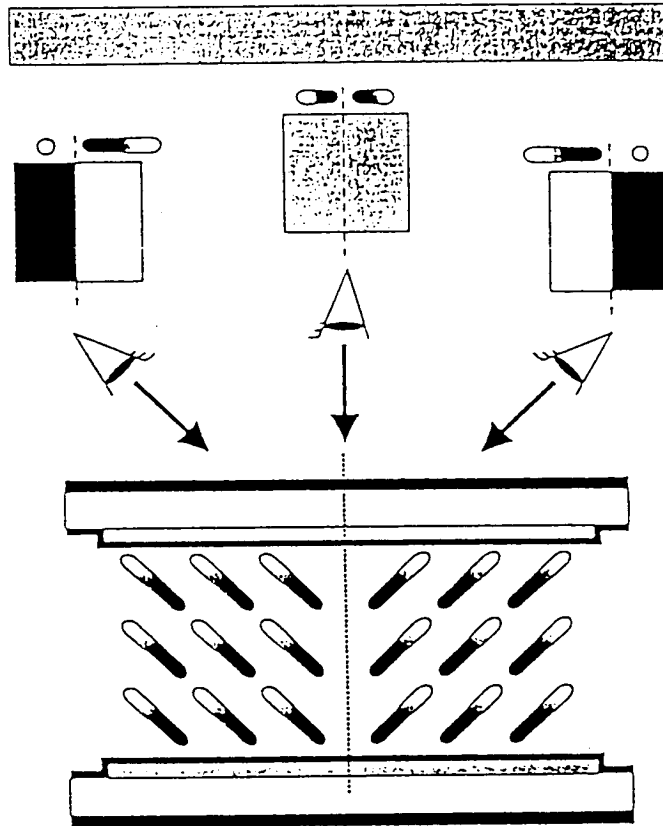
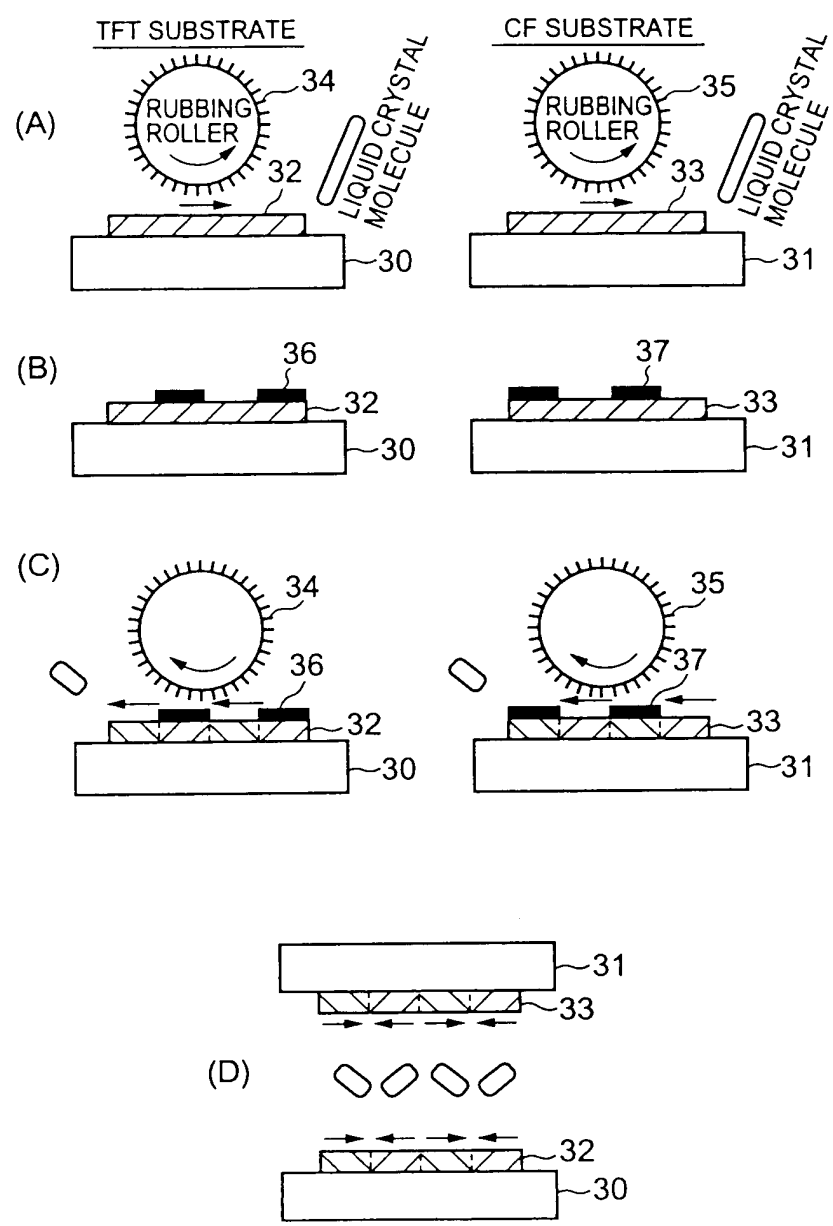


FIG.6



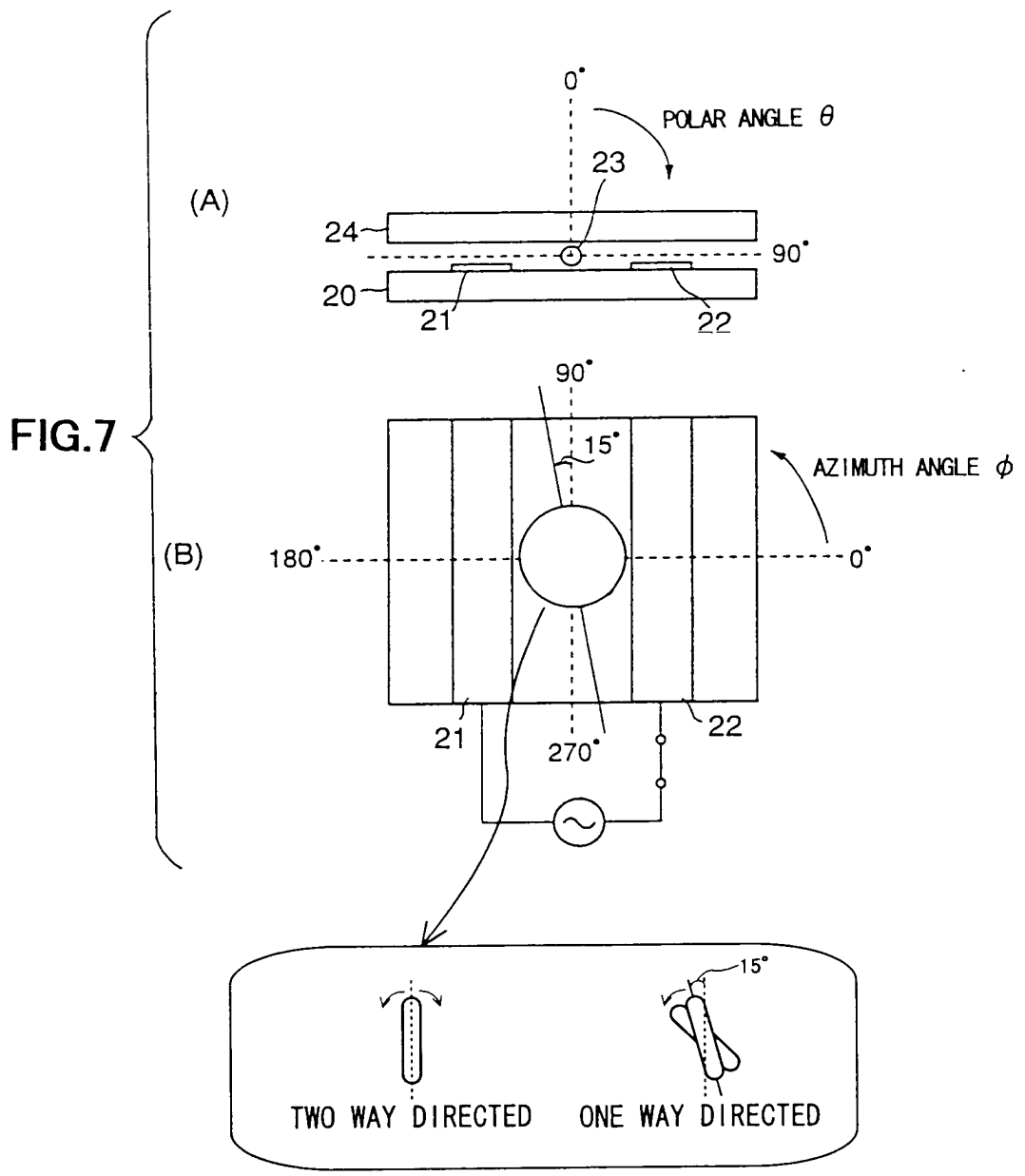


FIG.8

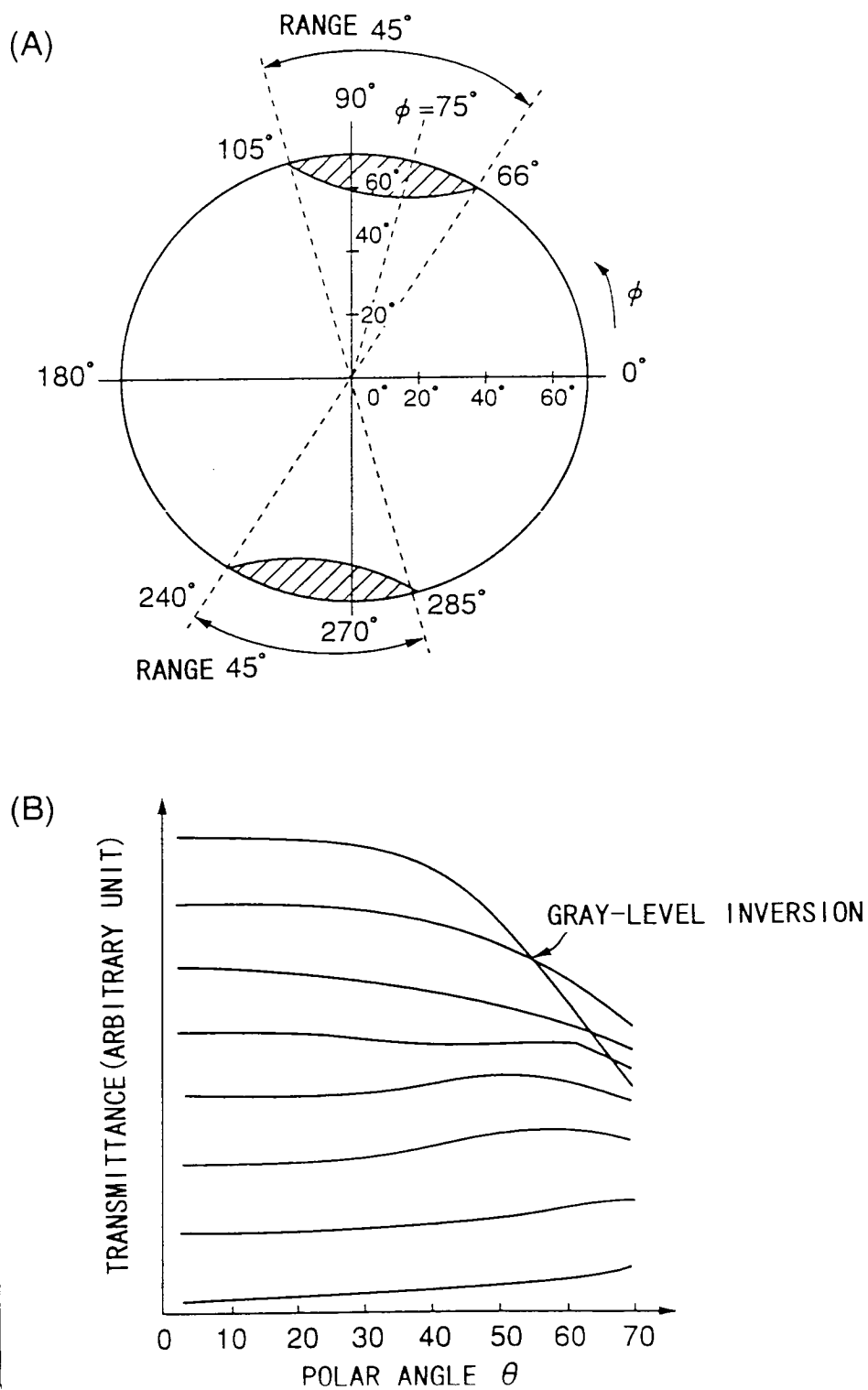




FIG.9

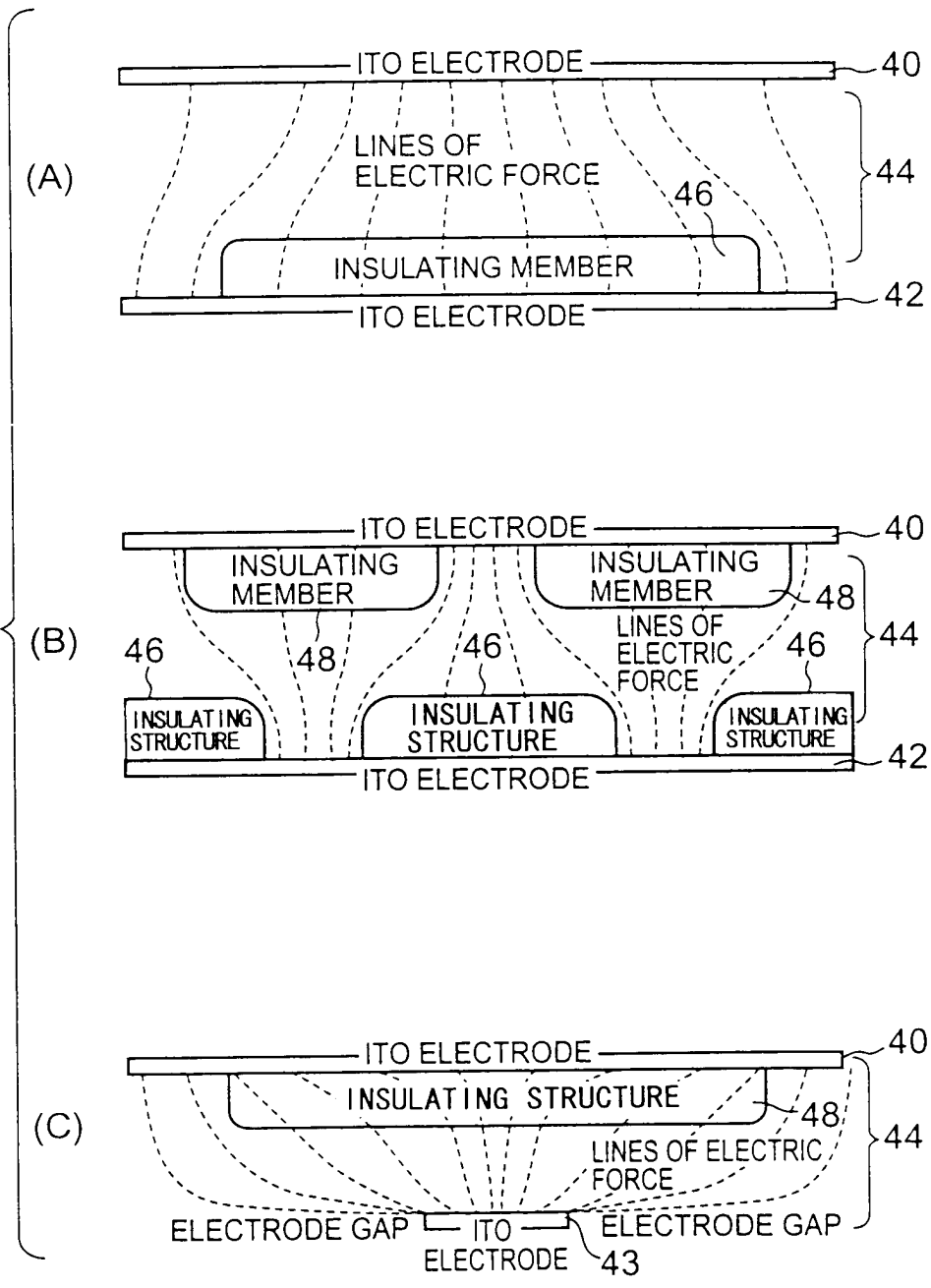


FIG.10

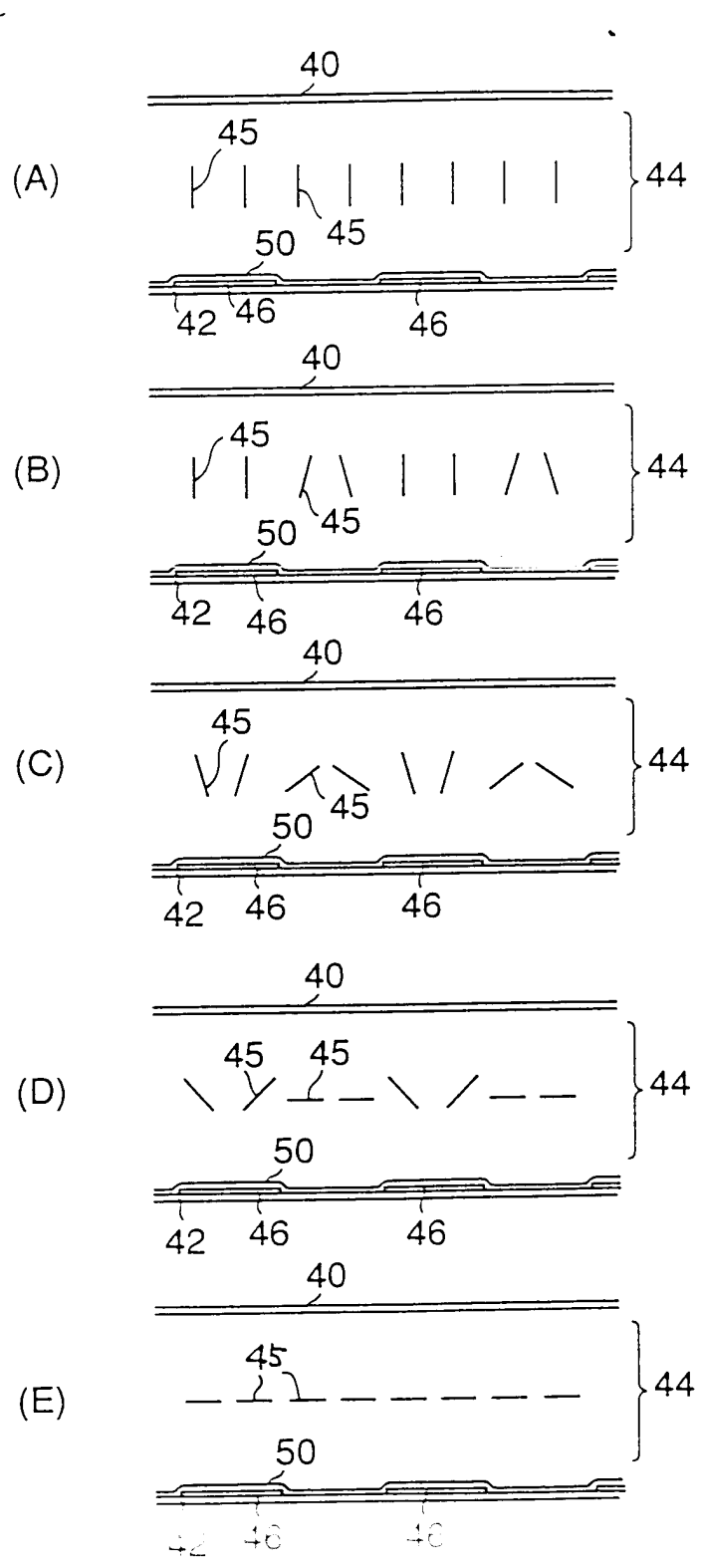


FIG.11

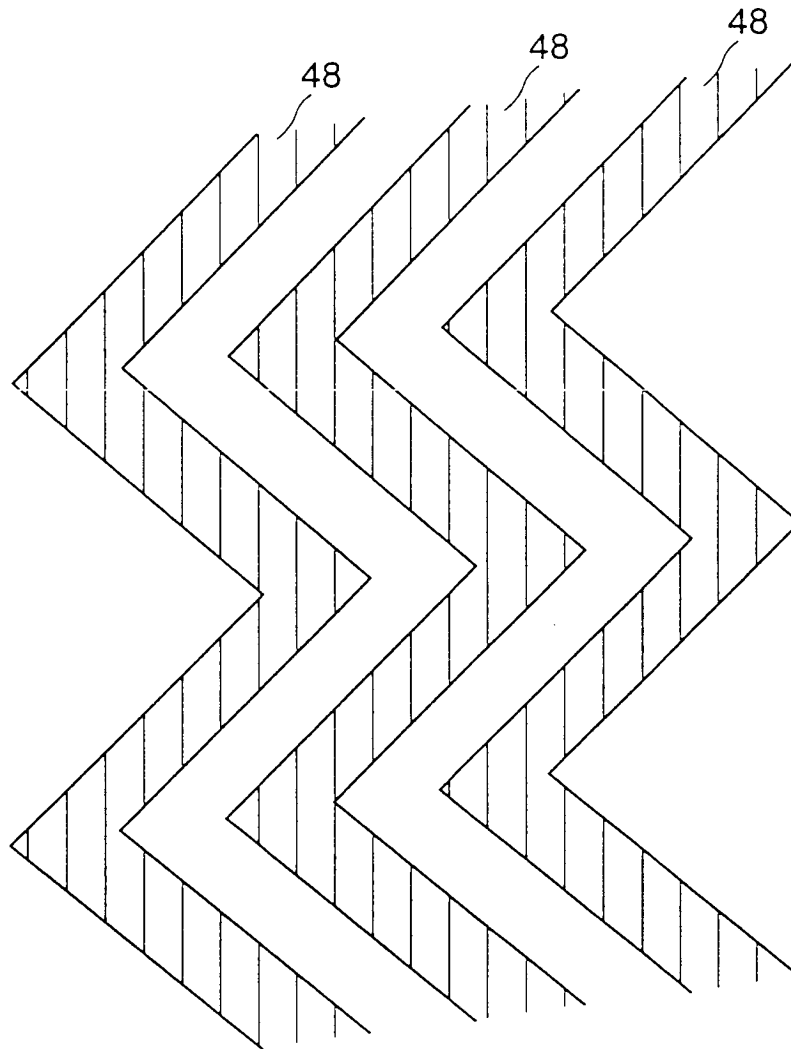


FIG.12

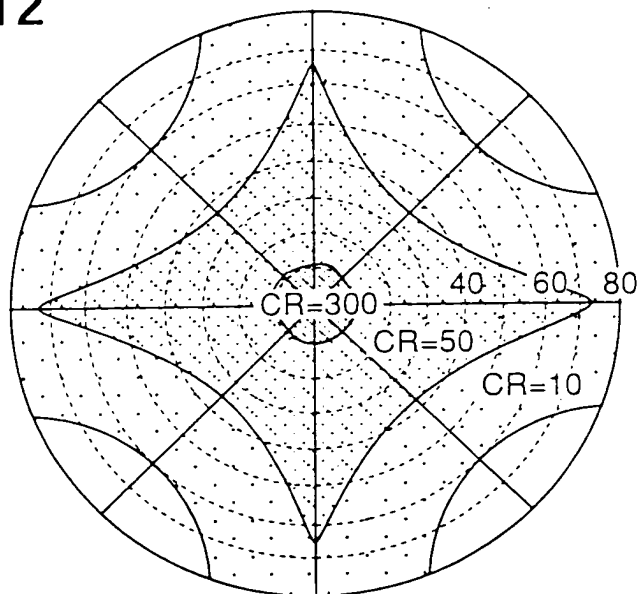


FIG.13

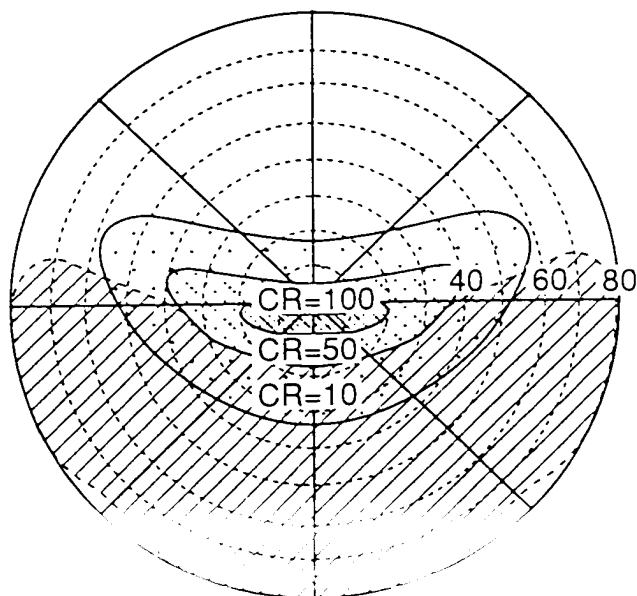


FIG.14

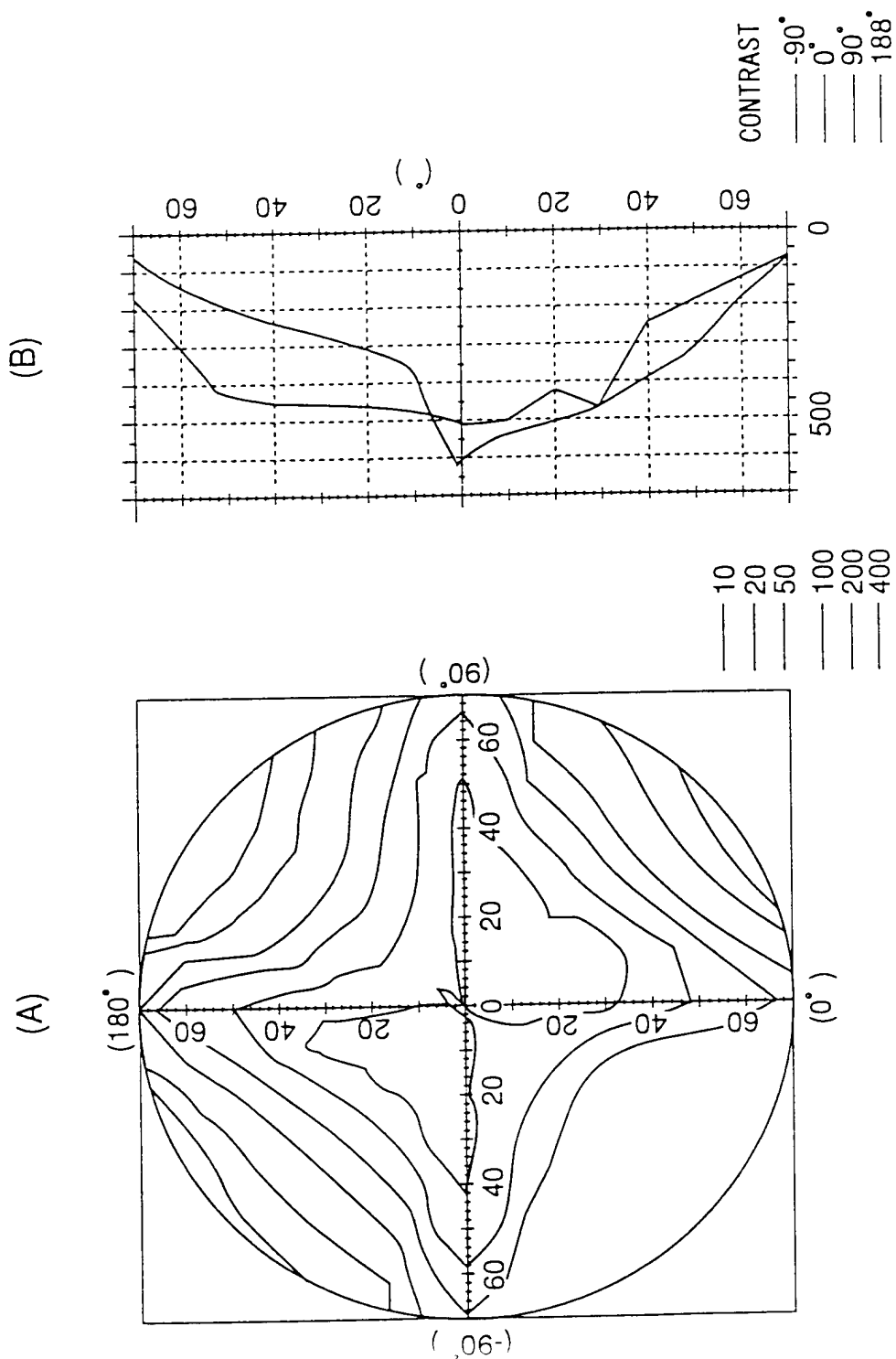




Diagram illustrating a cross-sectional view of a pixel structure in a liquid crystal display. The structure includes a TFT (Thin-Film Transistor) and a GATE BUS LINE (84). The pixel is divided into three vertical sections: 70, 71, and 72. The sections are separated by vertical bars (76, 77, 78). The entire pixel structure is labeled 68. A DATA BUS LINE (79) is also shown.

FIG.17

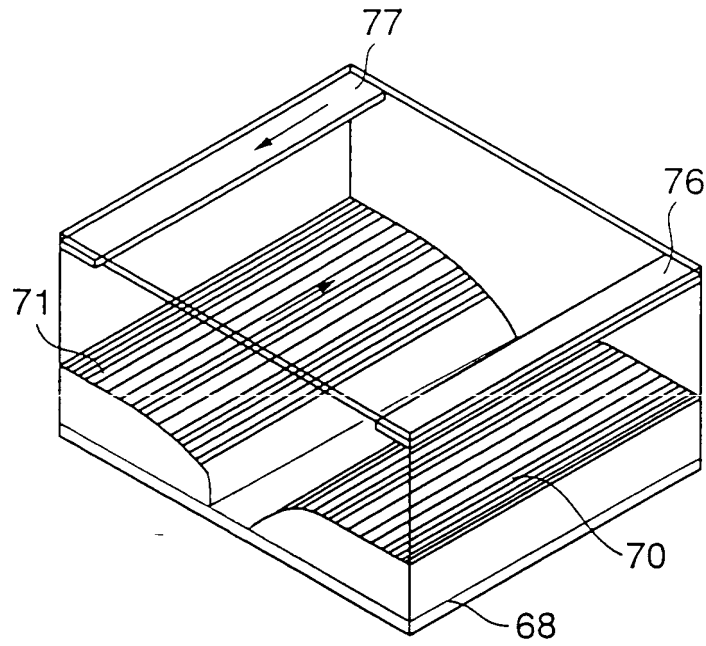




FIG.18

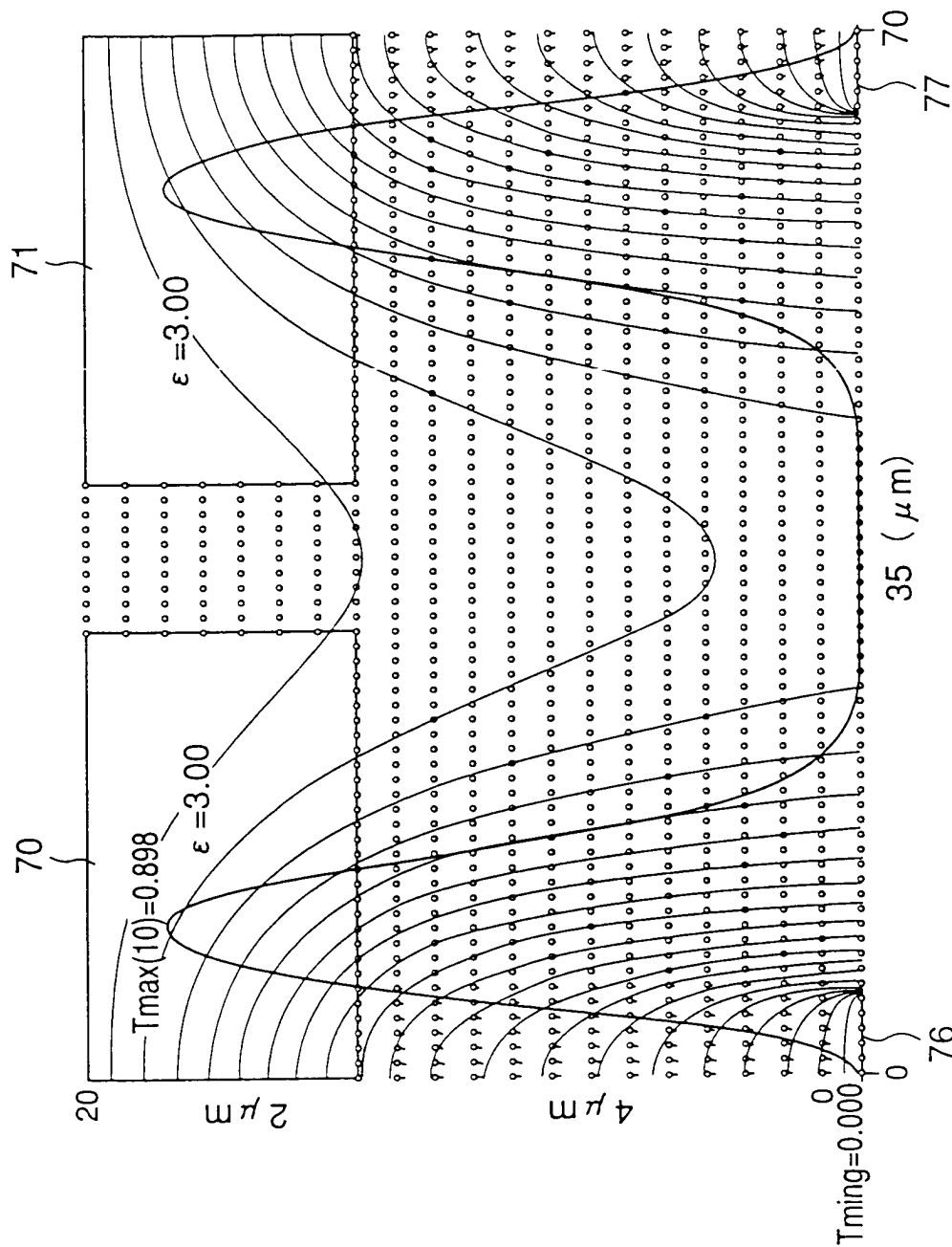


FIG.19

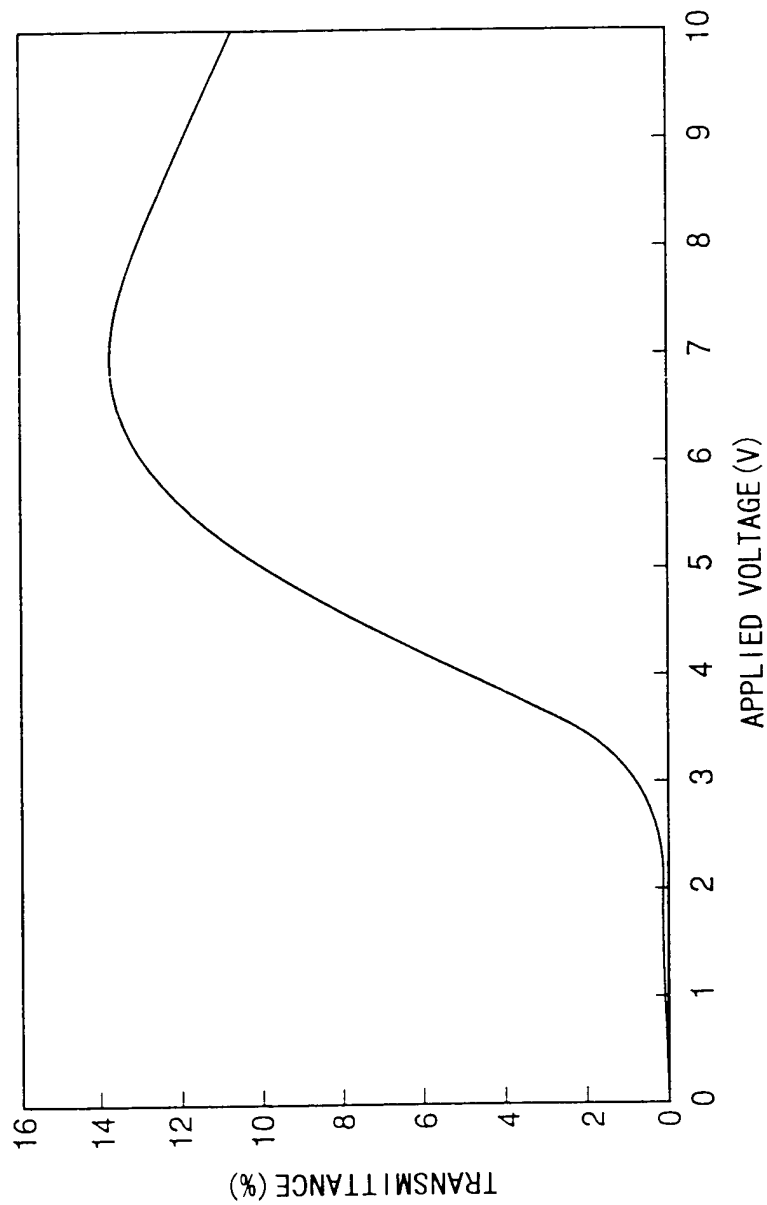


FIG.20

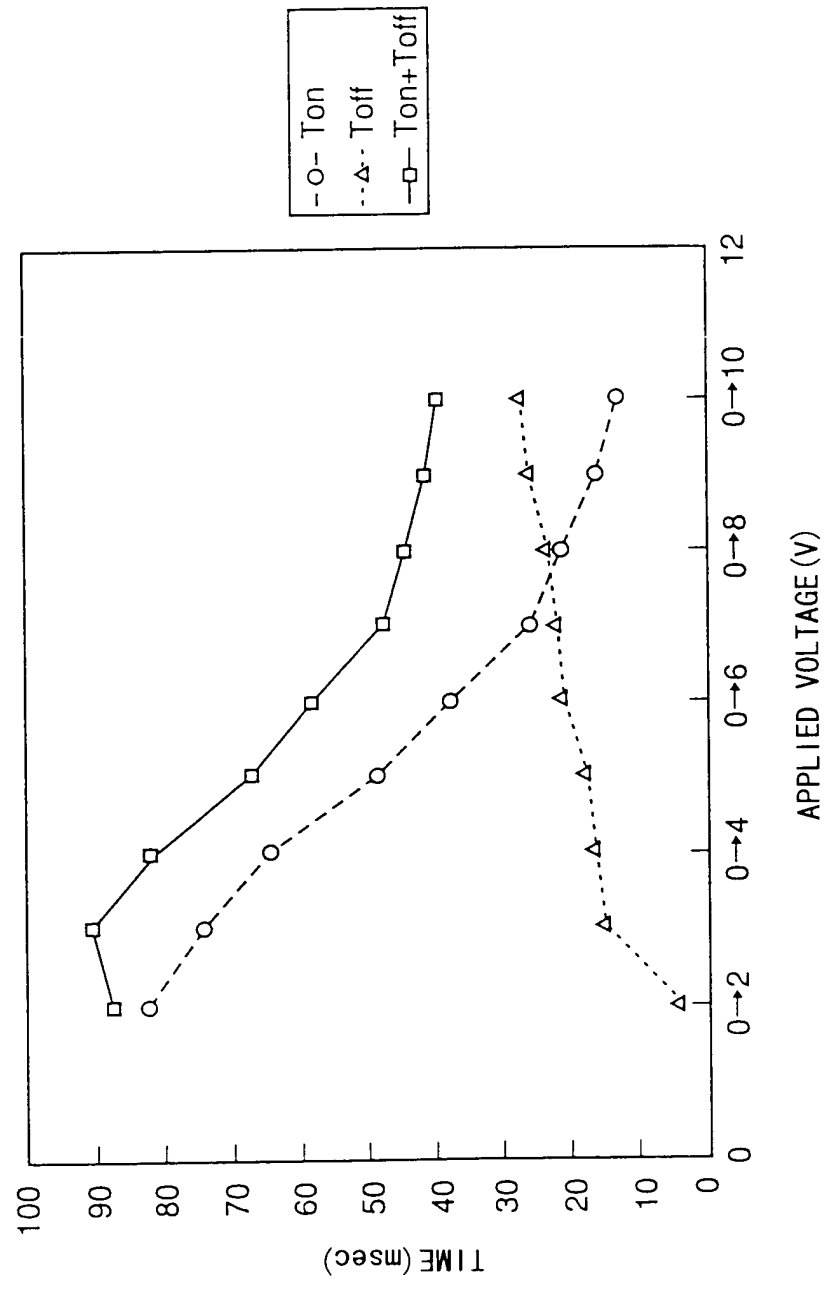
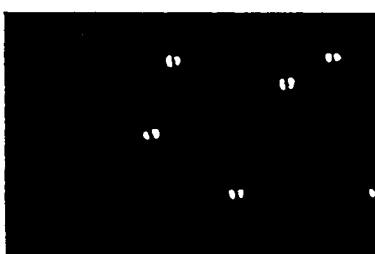


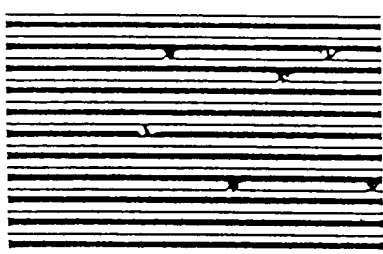
FIG.21

(A)



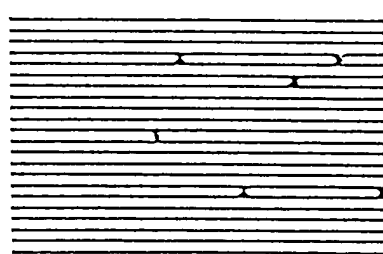
3.0V

(B)



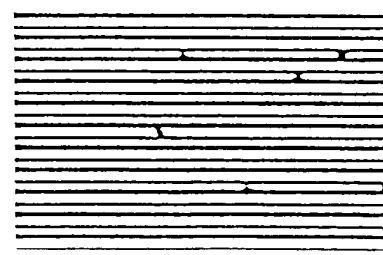
5.0V

(C)

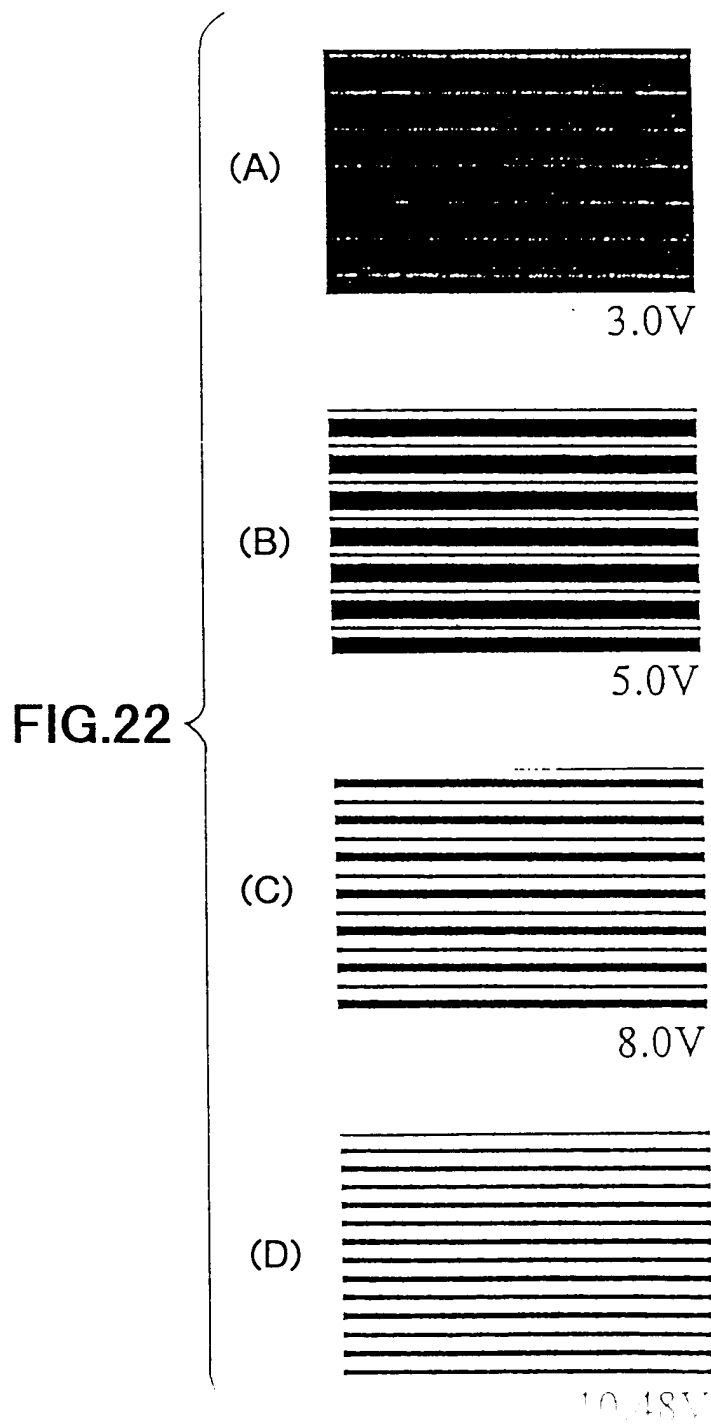


8.0V

(D)



10.48V



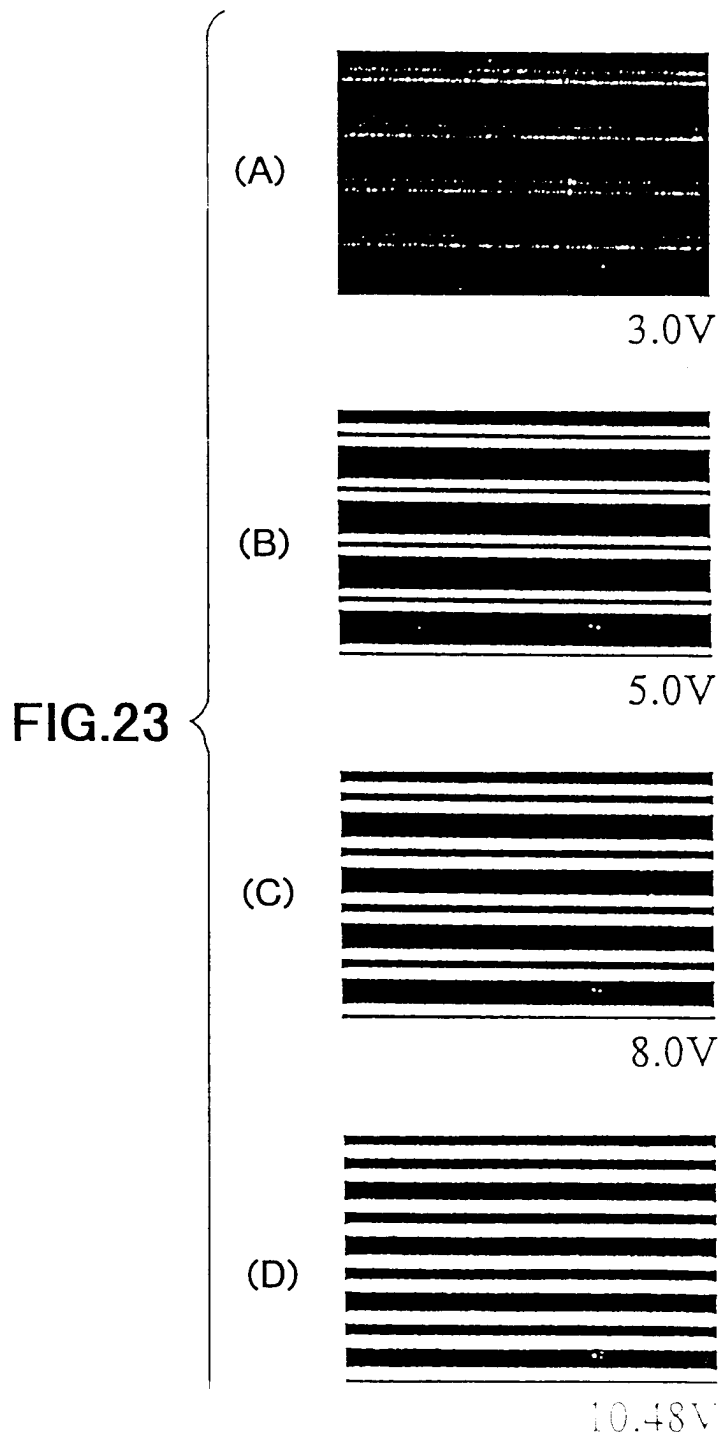


FIG.24

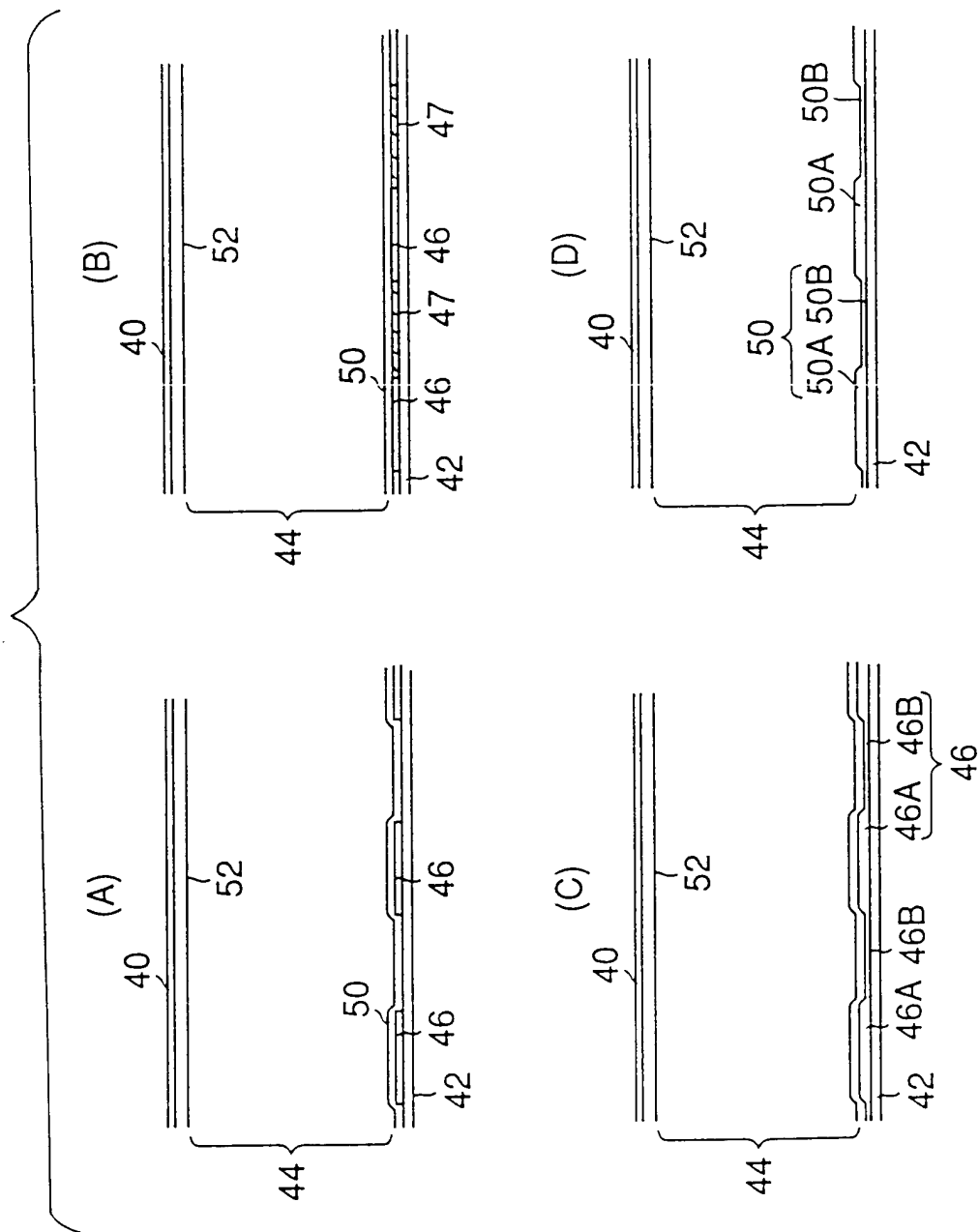


FIG.25

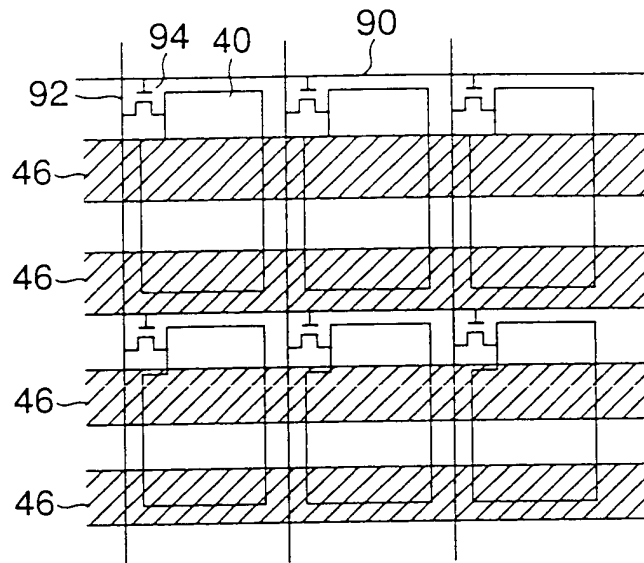




FIG.26

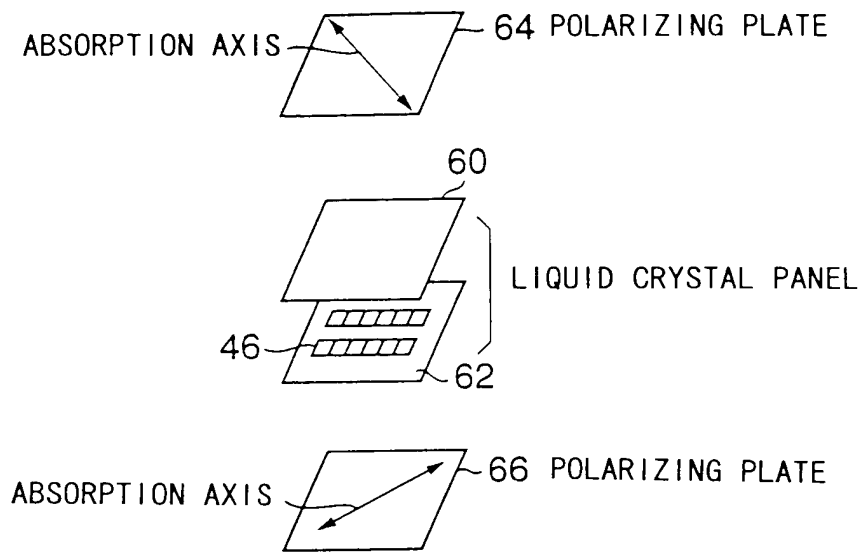


FIG.27

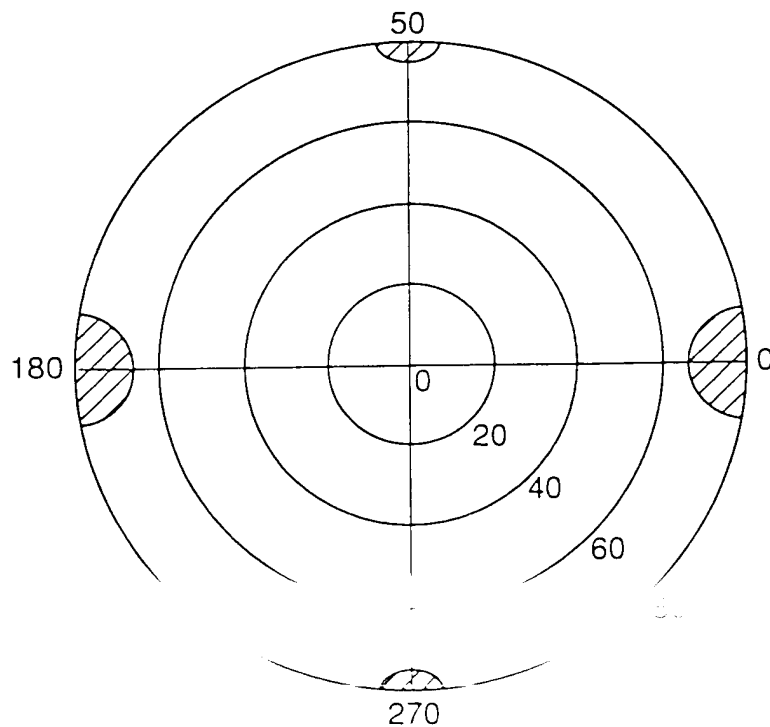


FIG.28

